

06/28/01

	Class	Subclass
ISSUE CLASSIFICATION		

PATENT NUMBER

## U.S. UTILITY Patent Application

<p>O.I.P.E.</p> <p>SCANNED <u>103</u> Q.A. <u>cm</u></p>	<p>PATENT DATE</p>
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APPLICATION NO. 09/895559	CONT/PRIOR	CLASS 714	SUBCLASS 7/8	ART UNIT 2133	EXAMINER D. J. [Signature] YUFA [Signature]
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## APPLICANTS

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ASD

**TITLE**

# Testing architecture for a semiconductor memory device

PTO-2040  
12/99

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ISSUING CLASSIFICATION												
ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

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	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
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<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		<b>ISSUE BATCH NUMBER</b>	
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